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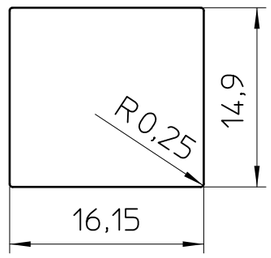
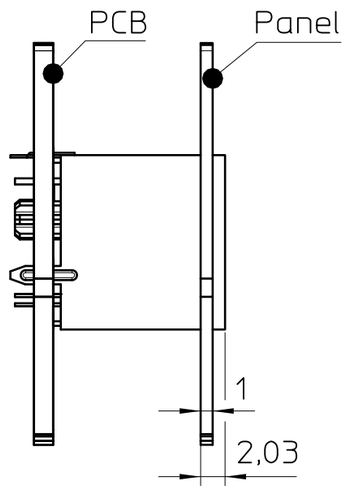
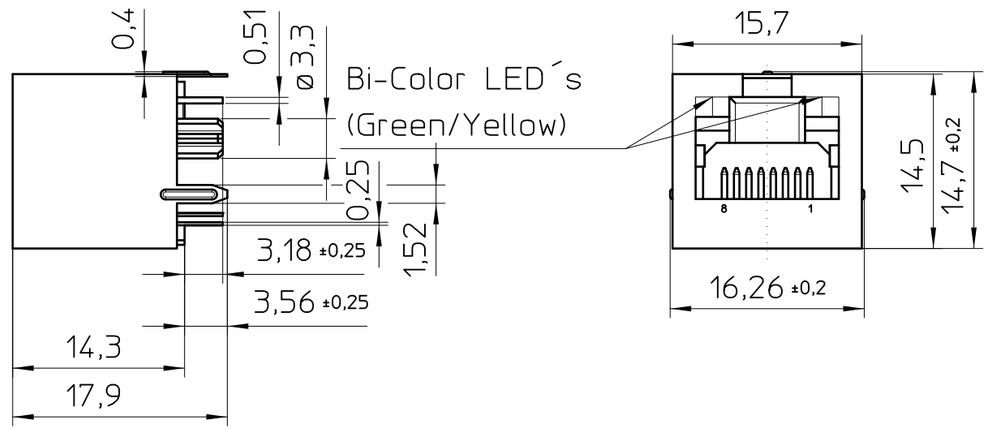
Tolerance specifications do not include clearances inherent in the design.

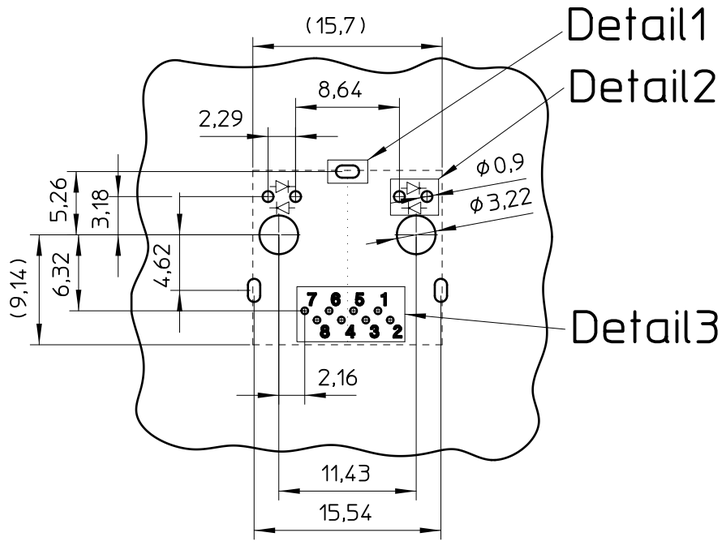
panel cutout



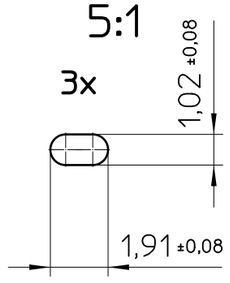




PCB drill hole dimensions
(seen from component side)

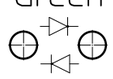


Detail1
5:1



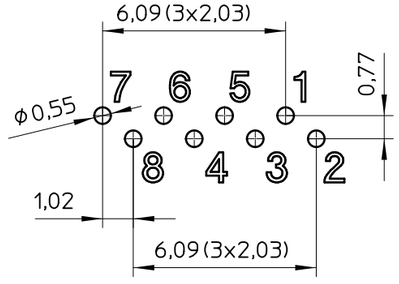
Detail2
5:1

2x
LED Polarity Detail



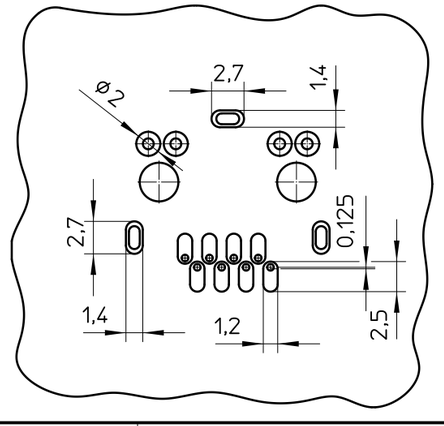
Detail3
5:1

Signal PIN Number Detail



PCB recommended layout for THR and solder wave
copper layer and solder paste mask (150 µm)

(seen from component side)



Recommended PCB Layout
(Tol: ±0,05, T=1,60±0,15mm)

LED- Specifications

Bi-Color LED		Forward Voltage (Max)	*Forward Voltage (Typ)	Wavelength
Green/Yellow	Green	2,5 V	2,2 V	565 nm
	Yellow	2,5 V	2,1 V	590 nm

* With a forward current of 20 mA (Typ)

- EN ISO 291 23/50

General Tolerances
1 Decimal Place ±0,25
2 Decimal Place ±0,13
3 Decimal Place ±0,05

DIN A3

PHOENIX CONTACT

page 1 of 1

document-no. / ri	article-no.	scale
01230415 / 06	1091947	2:1

date	document-type
14.01.2025	TECDOC 2D_Productfamily

description: CUC-IND-J1ST-S/R4LS-LED

- Simplified representation
- Document excluded from change process
- Linear dimensions [mm]

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